

### **Dual Channel 3.0 GSPS Analog to Digital Input Module**

#### Features

- 14-bit resolution
- Dual Channel 3.0 GSamples/sec
- 1GHz or Dual 500MHz Instantaneous BW
- Complex dual DDC output
- AC Coupled Input
- Analog Input nominal 0 dBm
- SFDR at max sample rate is >70 dB (See Figures 3,4,5)
- 3dB sample-and-hold Bandwidth 10 MHz 4.0 GHz
- External 1PPS input for precision time stamping of data
- External 10MHz reference input
- Dual I/O Site module for use with ICEPIC8 and 8<sup>th</sup> generation series Cards/Devices



Figure 1 ICE-A2D-m20 on a PCIe 8-Lane, PIC8L (Long)

#### Applications

- Multichannel, multimode receivers
- High Performance, Very Wideband Digital Down Conversion (DDC)
- Wideband Microwave, Wideband Radar
- Base Station CDMA, GSM, WCDMA Testing
- Direct Digital receivers
- Base station infrastructure
- Communications instrumentation
- Radar, infrared imaging
- Instrumentation, Test and Measurement

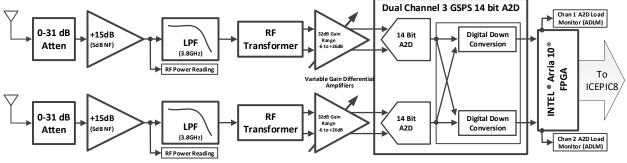


Figure 2 - Signal of Interest Flow for both channels

### **Product Description**

The ICE A2D-m20 is a dual channel 14-bit analog-to-digital converter input module for use with the PCIe Gen3 ICE-PIC8 and ICE 8<sup>th</sup> generation series of high-speed digitizer interfaces. There are two digital down converters accessible from either A2D channel. Further decimation and sub-band digital tuning of the DDC output is also available on the ICEPIC8 card. Various DDC decimation choices are available. Using the A2D-m20 on the ICEPIC8 DSP card can yield a maximum of two 500MHz Complex Integer bandwidths or one 1GHz Complex Integer bandwidth [Complex Integer (CI)] that can be processed in real-time through a single ICEPIC8 series device.

The on-board clock synthesizer uses a 10MHz reference from either external source or via an on-board temperature-controlled crystal oscillator (TCXO).

The use of external 10 MHz and 1 PPS reference signals allows for a disciplined reference and precision time tagging/stamping of digitized data.

The RF Power Reading is a value that estimates the input at the source compensating for the attenuator value.

The analog-to-digital load monitor (ADLM) provides input loading to allow optimization of the attenuation and amplification of the input signal.

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### **Input Specifications**

Parameter	Minimum	Typical	Maximum	Units
Analog Input (Both Channels)		-10 dBm	+5 dBm	dBm
Analog 3dB Passband	10.0		4000.0	MHz
External Clock Input <sup>1</sup>	0.5	1.0	2.0	V p-p
External Ref <sup>1</sup> (AC coupled)	0.5	1.0	2.0	V p-p
Analog Input Impedance (AC Coupled)		50		Ohm
Sample Clock Range	2.500		3.000	GHz

 Table 1
 Note: Operation using external reference of 10MHz replaces the external clock input.

 Sample clock is then generated from the 10MHz reference.
 10MHz reference.

### Absolute Maximum Ratings

Parameter	Value	Units	Conditions
Electrical AVcc DVcc PPS input External clock input Analog input power	2.0 2.0 0 to 3.3 0 to 2 +10	VDC VDC Vpeak into 50ohm V p-p into 50ohm dBm (50ohm ref)	VDC VDC TTL 3.3V Expected 50ohm dBm (50ohm ref)
Environmental Operating Temp Storage Temp	-10 to +65 -40 to +85	Degrees C Degrees C	

Table 2

*Warning:* Operation of this module beyond any of these parameters (temperature, voltage, and input power) may cause permanent damage. Exposure to absolute maximum ratings for extended periods may affect module reliability. Operation of the module beyond these specified parameters voids the factory warranty.



#### Two Tone Performance of ICE-A2D-m20

- Two tone performance
- Sample Rate (GHz) 3.0 GSPS
- Decimation = 6
- Bits/Sample: 14
- Typical Spurious Free Dynamic Range (SFDR): See Figure 3
- Effective Number of Bits (ENOB): ~11.6 bits
- Capture rate for 500 MHz Bandwidth 2 GBytes/sec
- Capture rate for Dual 500 MHz Bandwidth 4 GBytes/sec
- Recorders capable of Dual 4 GBytes/sec are available from ICE

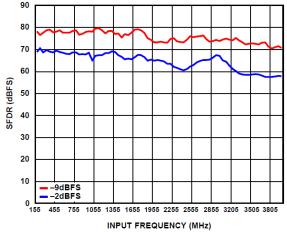


Figure 3 SFDR profile

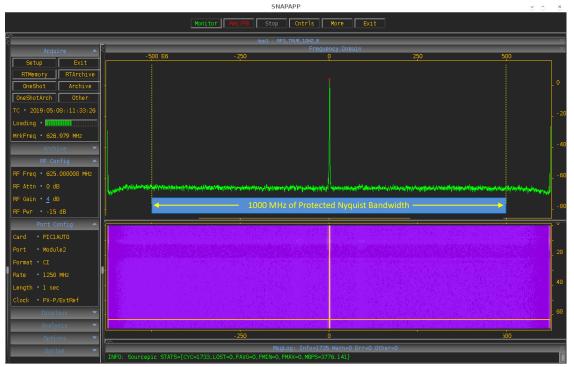


Figure 4 - Typical >70dB SFDR in 1GHz of protected Nyquist bandwidth

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Two Channels, each with 500MHz of protected Nyquist bandwidth

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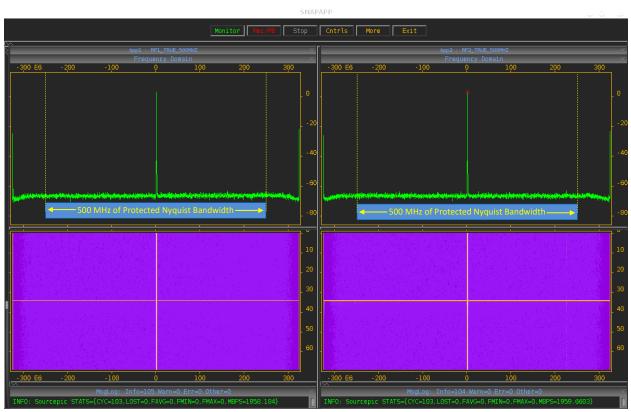


Figure 5 - Dual Channel Typical >70 dB SFDR in 500MHz of protected Nyquist bandwidth



### Connectors

Connector	Manufacturer	Part Number	Description	J-Number
Signal Connections Analog In 1 Analog In 2 Clock or Reference In 1 Pulse Per Second (PPS) In Board Connection Data Connection Power Connection Physical Connection Standoff (2 each)	Hirose Hirose DeltaRF DeltaRF Samtec	HRM(G)-300-467B-1 HRM(G)-300-467B-1 2467000G91P-050 2467000G91P-050 QTE-020-03-F-DA	Edge Mount SMA Connector Edge Mount SMA Connector Edge Mount SMB Connector Edge Mount SMB Connector 40 Pin Connector Standard 2-pin Header Physical Connection	J5 J7
Mating Connectors SMB (J1, J4) Data (J3, J6)	Amp Samtec	# 1-5414946-1 QSE-020-01-F-D-A	40 Pin Connector	J3, J6 Mate

#### Table 3

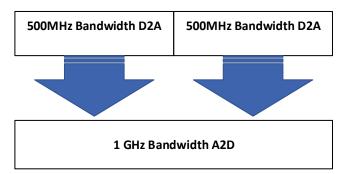
#### **ICE Interface Cables**

Rigid well shielded RF rated SMA cabling is recommended for best signal quality.

For SMA mating cables we recommend SMA cables with proper frequency response to at least 4 GHz for operation at full "sample and hold" bandwidth.



Typical Application shown in the figure and display below.



Dual 500MHz Transmit with ICE-D2AWGm3 (Dual 2GBytes/sec Transfer outbound)

1 GHz Receive/Record with ICE-A2Dm20 (Single 4GBytes/sec Transfer inbound)



Figure 6 - PIC8 #1 with Dual D2AWGm3 Output Modules / PIC8 #2 with A2Dm20 1GHz BW Input A2D clock rates of 2500 MHz and 2600 MHz provide Nyquist protected bandwidth of 1000 MHz and 1040 MHz respectively.

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#### Example of Pre-select Filtering for Nyquist Zones 1 (10-1250MHz) and 2 1250-2500MHz)

The inbound bandwidth to the A2Dm20 front-end is 10-3800 MHz. All frequencies with this range are available. The availability of all frequencies between 10 and 3800MHz occurs when a broadband antenna / amplifier combination is used as the analog input to the digitizer. There are two digital down convertors used to consume pre-detected data from the digitizer. In order to digitize and capture data, a sampling rate of 2500 Mega-samples/sec is used. When using this sampling rate, *without* pre-select filtering of the 10-3800MHZ input frequency spectrum, all spectral data from 1250MHz to 2500MHz will fold down on top of spectral data from 0-1250 MHz. All folded signals in the 1250-2500 MHz range will be inverted in frequency and mixed and inter-modulated with the 0-1250MHz signals. This is undesirable. This 1250MHz point in the frequency spectrum is referred to as the "fold point" or the [Sampling Frequency / 2, aka Fs/2]. The range of 0-1250MHz is often referred to as Nyquist Zone 1. The range of 1250-2500MHz is referred to as Nyquist Zone 2. For this reason, pre-select filters isolating these Nyquist Zones must be used (see figures 7 and 8).

Alternatively, if the input to the A2Dm20's analog front-end is from an already filtered input, then preselect filters are unnecessary. For example: A 500MHz bandwidth receiver IF at 1.0GHz center frequency or a block down converter with 1200MHz bandwidth at 1875MHz center frequency. Occasionally a lowpass filter may be used if necessary for spectral purity.

When using the A2Dm20 for single 1.0 GHz bandwidths a digitizing sample rate of 2500 Mega-samples/sec is used. The 1<sup>st</sup> Nyquist zone is from 10-1250 MHz for 2500MSps and 10-1500 for 3000MSps. This 1<sup>st</sup> Nyquist zone limit is simply the sampling rate divided by 2.

The front-end bandwidth to the A2Dm20 is wide band: 10-3800 MHz. To properly digitize a signal that is within the first Nyquist zone a pre-select low pass filter (LPF) must be used. This LPF rejects signals in 2<sup>nd</sup> Nyquist zone and stops them from folding down on top of the first Nyquist zone. An example of this pre-select LPF is shown in figure 7 below.

The below diagram shows a very sharp cutoff low pass filter (Figure 7) with a cutoff at 1370.59 MHz. The sharp cut off requires cascading two 10 Pole, lumped element filters achieving a clear unoccupied spectrum above 1370.59 MHz at 50dB down. A digitize rate of at least twice this cutoff frequency is desirable for accurate spectrum capture. The sampling rate of the A2Dm20 is called the RFRate. To maintain Nyquist, an RFRate from 2750 to 3000 is required.

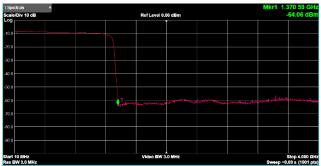


Figure 7 Low Pass Filtering of Nyquist Zone 1

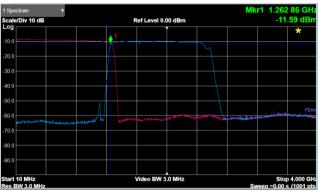


Figure 8 Band Pass Filtering of Nyquist Zone 2

# ICE Enterprises Entering The 3rd Decade of Innovative Solutions

